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RADEMBRAD		Application Number	10/619,738			
		Filing Date	July 14, 2003			
TRANSMITTAL FORM		First Named Inventor	Sabhapathi Naidu Annamaneni			
(to be used for all correspondence during per filed application)	ndency of	Group Art Unit Number	2812			
		Examiner Name	Not yet known			
Total Number of Pages in This Submission	5 *	Attorney Docket Number	22272-08030			
FNCLO	SURES	(check all that apply	v)			
Fee Transmittal Form (in duplicate) Check Enclosed Return Receipt Postcard Response to Notice to File Missing Parts Assignment & Recordation Cover Sheet Declaration Power of Attorney Application Data Sheet Information Disclosure Statement & PTO Copies of IDS Cited Reference Request for Corrected Filing Receipt Request for Correction of Recorded Assi Amendment/Response: [] Page(s) After Final Status Request Revocation and Substitute Power of Attorney	o/SB/08A es gnment	Issue Fee Trans Letter to Chief D Formal Drawing(I Sheet(s) Appeal Commun Interferences Appeal Commun (Appeal Notice, Certified Copy of	mittal raftsperson (s): s) of Figure(s) [] nication to Board of Appeals and			
REMARKS: * Does not include pages in refer	rence(s).					
SIGNAT	URE OF	ATTORNEY OR AGE	NT			
Signature: Michaelle J						
Attorney/Reg. No.: Michael W. Farn, Reg.	No. 41,015	5	Dated: 7.W. 20 , 2005			
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Sabhapathi Naidu Amananeni and Sarangkumar Sharadchandra

Bhavsar

APPLICATION NO.:

10/619,738

FILING DATE:

July 14, 2003

TITLE:

System and Method for Automating Integration of Semiconductor

Work in Process Updates

EXAMINER:

Not yet known

GROUP ART UNIT:

2812

ATTY. DKT. NO.:

22272-08030

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SIR:

Pursuant to the provisions of 37 CFR §§ 1.56 and 1.97-98, enclosed herewith is modified form PTO/SB/08A listing references for consideration by the Examiner.

The filing of this Information Disclosure Statement shall not be construed as a representation regarding the completeness of the list of references, or that inclusion of a reference in this list is an admission that it is prior art or is pertinent to this application, or that a search has been made, or as an admission that the information listed is, or may be considered to be, material to patentability, or that no other material information exists, and shall not be construed as an admission against interest in any manner.

This Information Disclosure Statement is being filed:

\boxtimes	within three months of the filing date of the application, or date of entry into
	the national stage of an international application, or before the mailing date of
	a first office action on the merits, whichever event last occurred;
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before the mailing of a first official action after the filing of a request for
continued examination (RCE) under 37 CFR § 1.114;

Ш			ational state in a sinternational analysis of the date of
	-		ational stage in an international application, or after the mailing
			est official action on the merits, whichever event last occurred, but
			tiling date of the first to occur of either: (1) a final action under
		•	13; or (2) an action that otherwise closes prosecution in the
	applic	ation, a	nd:
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		. Appl	icant certifies pursuant to 37 CFR § 1.97(e) that:
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			Disclosure Statement was first cited in a communication from
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			Disclosure Statement was cited in a communication from a
			foreign patent office in a counterpart foreign application and,
			to the knowledge of the person signing this certification after
			making reasonable inquiry, no item of information contained
			in this Statement was known to any individual designated
			under 37 CFR § 1.56(c) more than three months prior to the
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	Enclos		•	listed reference that may be material to the examination of
				ich there may be a duty to disclose.
	_	-		er 35 U.S.C. § 120, on the earlier filing date of prior
				ed on, and the references cited therein are hereby
				ired to be provided in this application under 37 CFR §
	1.98(d		1	T
\boxtimes	`	•	on was filed a	fter June 30, 2003. Therefore, pursuant to the waiver of
				CFR 1.98(a)(2)(i), copies of each U.S. Patent and each
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			atement.	1
Consi				es and favorable action are solicited.
				Respectfully submitted,
				SABHAPATHI NAIDU ANNAMANENI et al.
				$\mathcal{L}_{\mathcal{L}} \cap \mathcal{L}_{\mathcal{L}} \cap \mathcal{L}_{\mathcal{L}} \cap \mathcal{L}_{\mathcal{L}}$
	JA	N. Ze	, 2005	By: phobaelu, d
				Michael W. Farn, Reg. No. 41,015
				FENWICK & WEST LLP Silicon Valley Center
				801 California Street
				Mountain View, CA 94041
				Tel: (650) 335-7823
				Fax: (650) 938-5200

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Substitute for form 1449A/PTO	Complete if Known		
INFORMATION DISCLOSURE	Application No.	10/619,738	
STATEMENT BY APPLICANT	Filing Date	July 14, 2003	
378 PEMICIONI	First Named Inventor	Sabhapathi Naidu Annamaneni	
and w	Art Unit	2812	
14N 2 1 2005 33	Examiner Name	Not yet known	
Sheet 1 0 of 1	Attorney Docket Number	22272-08030	

U.S. PATENT DOCUMENTS					
		Document No.			
Examiner Initials*	Cite No.1	Number – Kind Code ² (if known)	Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	

FOREIGN PATENT DOCUMENTS					
		Foreign Patent Document			
Examiner Initials*	Cite No.1	Country Code ³ – Number ⁴ Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	T ₆

		OTHER REFERENCES - NON-PATENT LITERATURE DOCUMENTS	
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	1	Hagen, Dr. Klaus ten et al., <u>Technical Overview: WIP Tracking IC Manufacturing workflow and Cycle-time Reduction</u> , eSilicon Corporation, Sunnyvale, USA. Available Aug. 6, 2001 at http://www.esilicon.com , 25 pages.	

Examiner Signature	Date Considered	

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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